

PHYS3360/AEP3630

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Lecture 37

DAC specifications

- bit resolution
- accuracy
- linearity
- settling time

ADC (A/D converters)

staircase:

problems with staircase :

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b) susceptible to noise

successive approximation ADC

bits are set starting from _____

1)

2)

3)

4)

Ex 3-bit ADC , 0-7V

$$V_m = 2.5V$$

good:

bad:

Flash ADC

- resistor ladder

- input v_m

- _____ encoder

good:

bad:

Integrating ADC

Dual slope ADC

1)

$$v_1 =$$

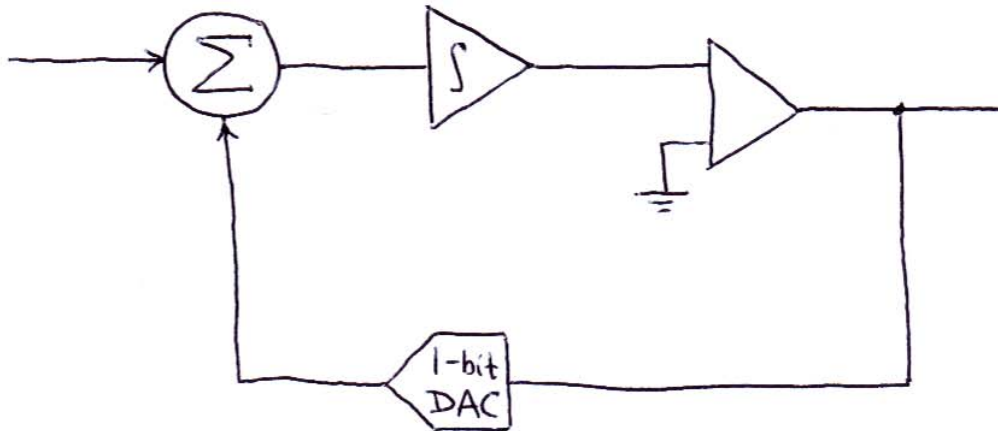
2)

slope \propto input voltage

good:

bad:

Lecture 38

 Σ - Δ modulator

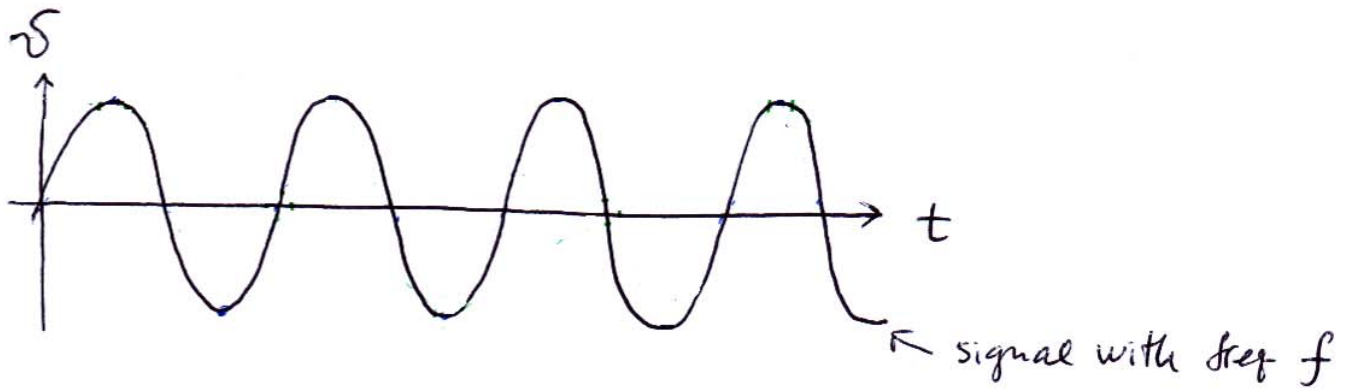
- Σ - Δ name
- unlike other ADC types,
- negative feedback theory:
- (parallel) binary output is obtained

Refer to the schematic

- op-amp #1
- op-amp #2
- op-amp #3

Nyquist sampling criterion & aliasing

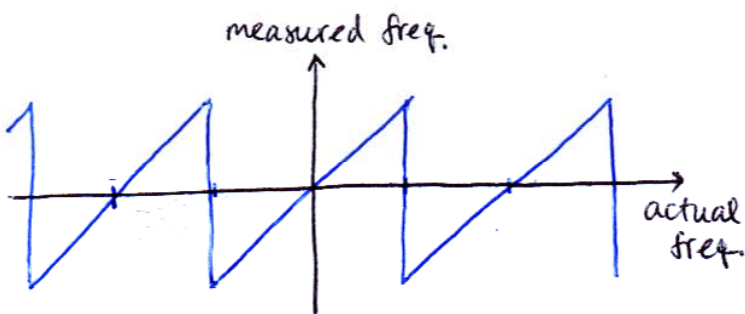
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- if

- if

- if



Effect of conversion time (t_c) on resolution

$$v_m = v_o \sin(2\pi f t)$$

$$V_{p-p} = 2v_o$$

E.g. 8-bit ADC with $t_c = 10\mu s$ (100kHz)

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Sample & hold circuit

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General A/D converter layout

Digital scopes

Sampling

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Real-time

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PHYS 3360 / AEP 3630

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Lecture 39

Electrical Noise

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Signal to noise ratio

$$SNR_{dB} =$$

Noise figure

$$NF_{dB} =$$

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E.g. 1dB noise figure :

Sources of Noise

I. Intrinsic

II. Interference

Intrinsic sources of noise

1) Johnson (thermal) noise



$$(\sigma_n)_{\text{rms}} =$$

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- "white noise"

- "gaussian noise"

Ex: $R=20k\Omega$ @ $T=300K$, $B=20kHz$

2) Shot noise

Ex: $i_s = 1A$, $B = 20kHz$

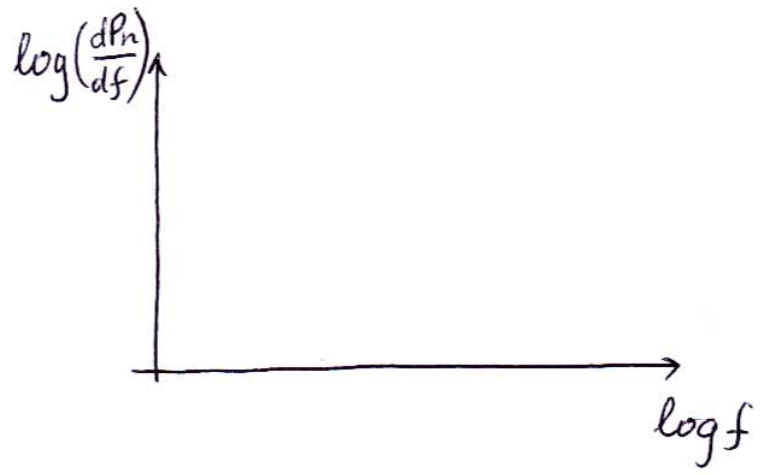
$i_s = 10pA$, — " —

- also

3) $1/f$ (flicker) noise

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-
- a.k.a.
-

To minimize intrinsic noise

-
-
- limit
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