

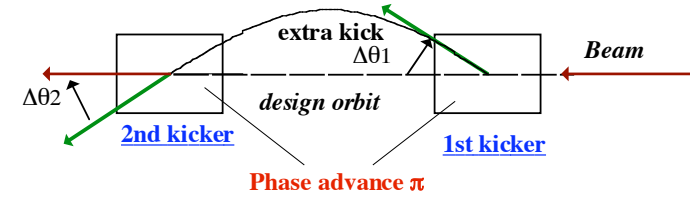
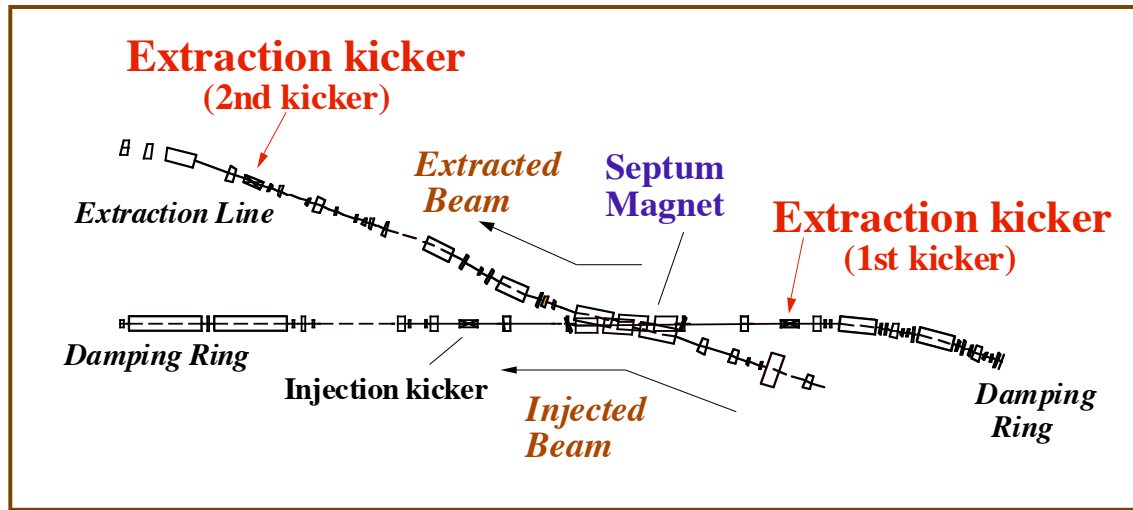
Beam Extract by using Strip-Line Kicker

at KEK-ATF

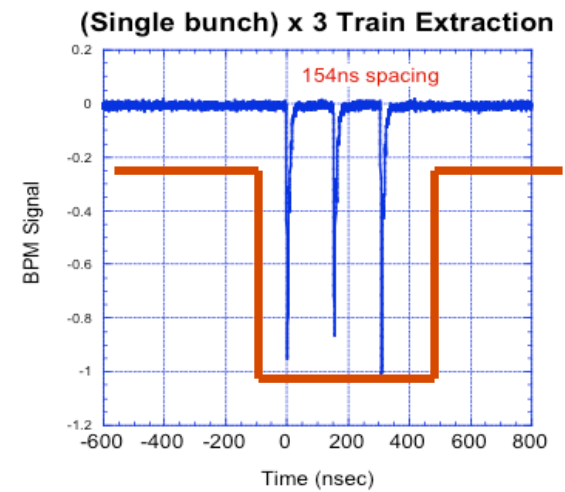
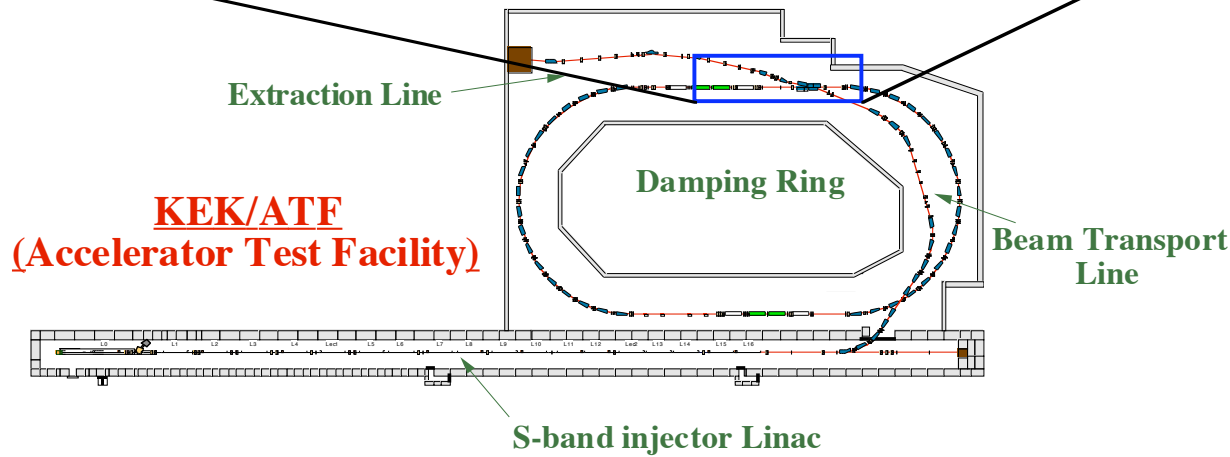
20060531 T.Naito

- Present kicker system
- Test result from the Beam Oscillation in the DR
- Experiment setup

Double kicker system



Kick angle Stability
 $1 \times 10^{-3} \rightarrow 2.8 \times 10^{-4}$

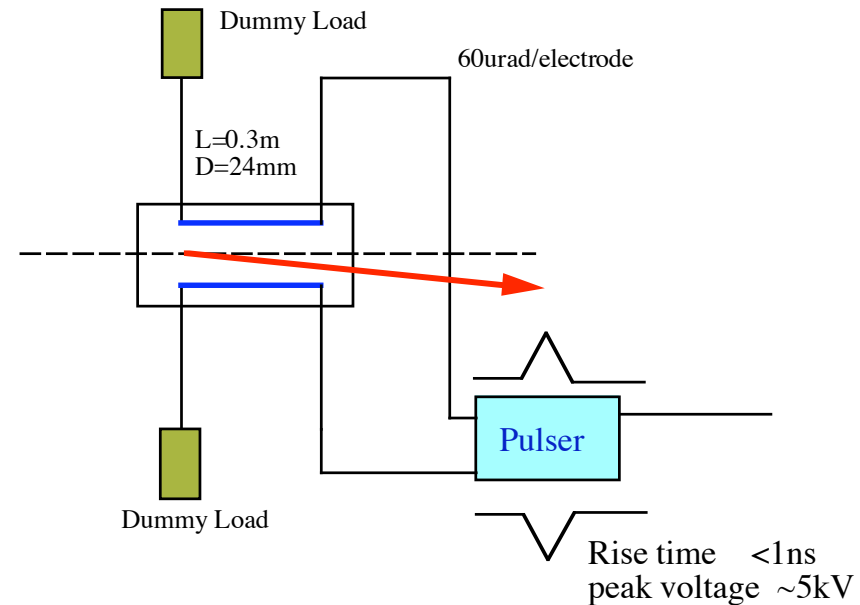


300ns

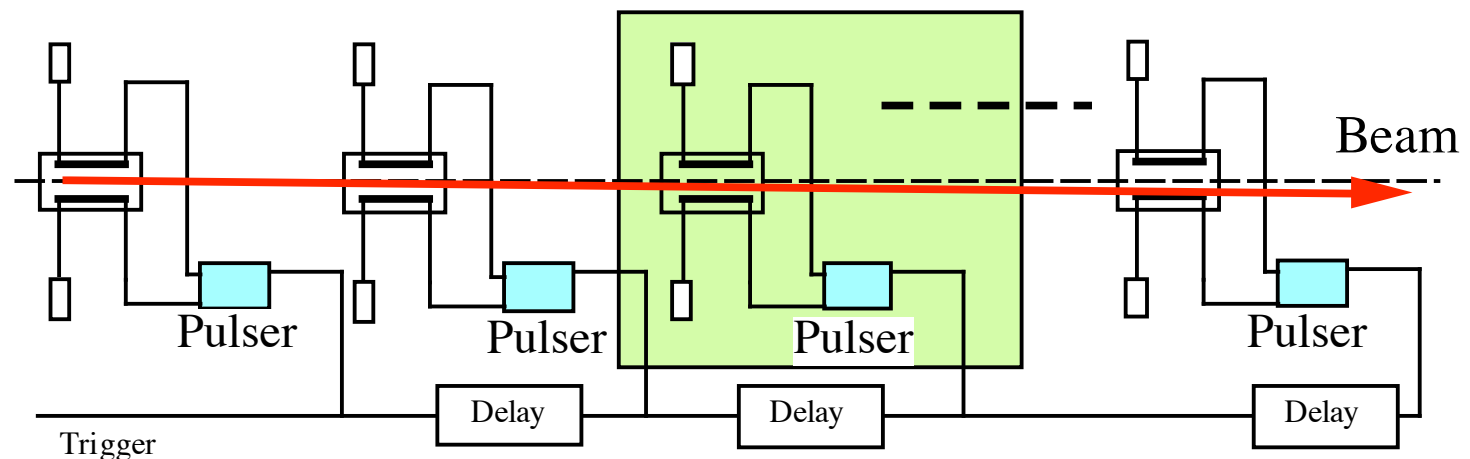
kicker system

The kicker unit, which consist of the strip-line electrode and the fast high voltage pulse power supply, makes the very fast kick field, $\sim 3\text{ns}$ rise/fall time.

20~40 units will be used to get the total kick angle(0.6mrad) at 5GeV , $\beta=50\text{m}$.

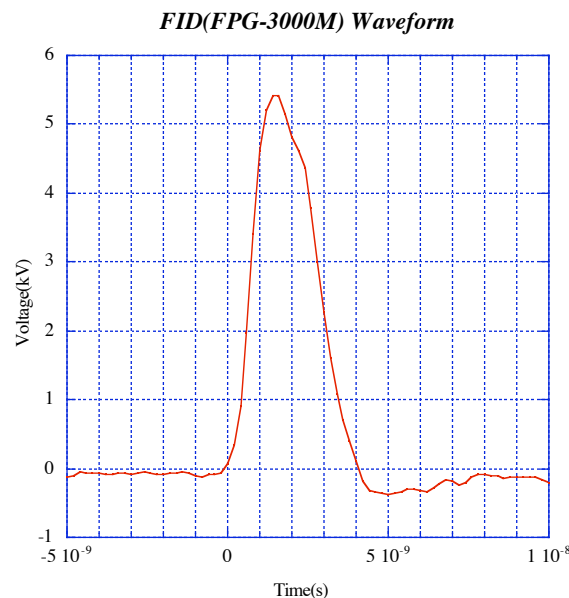


Single Unit



Pulse generator

We tested some of pulse generators, FID, Behlke, LLNL. FID Technology has very fast and high repetition rate pulse generators. The specification meets our requirement for the high voltage pulse source. We tested the kicker performance by using the pulser.



Specifications

Amplitude at 50 ohm : 5 kV

Rise time : 1-1,2 ns

Pulse width at 50% of amplitude : 2-3 ns

Maximum PRF in burst mode - 3 MHz

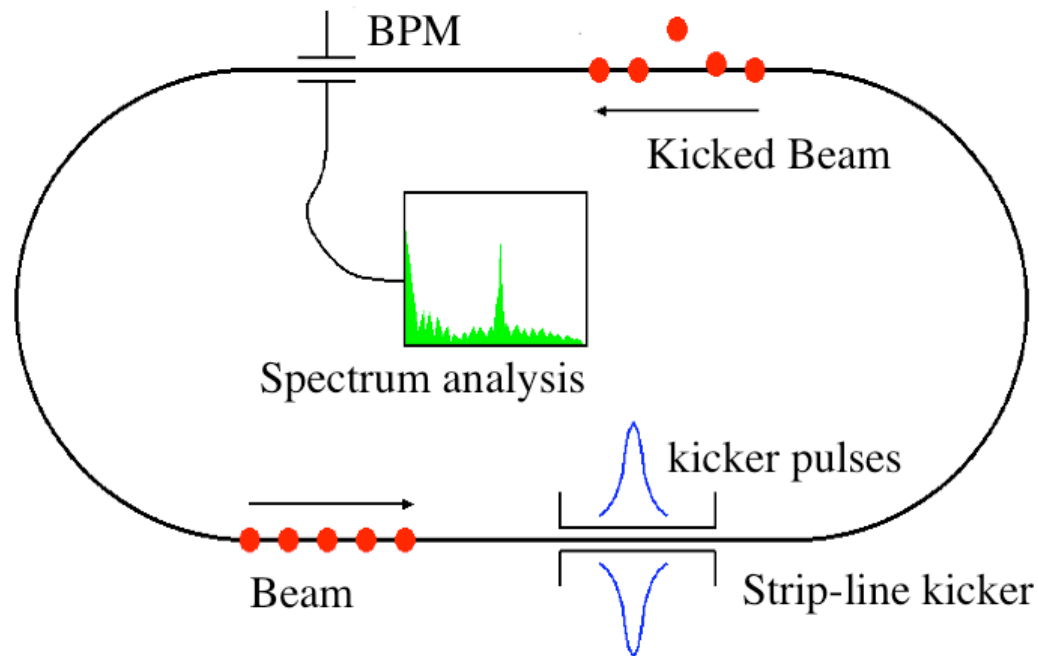
Beam kick test in ATF DR

We fabricated the single unit of the strip-line kicker.

The kicker pulse is applied to the strip-line electrode at just the time of the beam goes through the electrode.

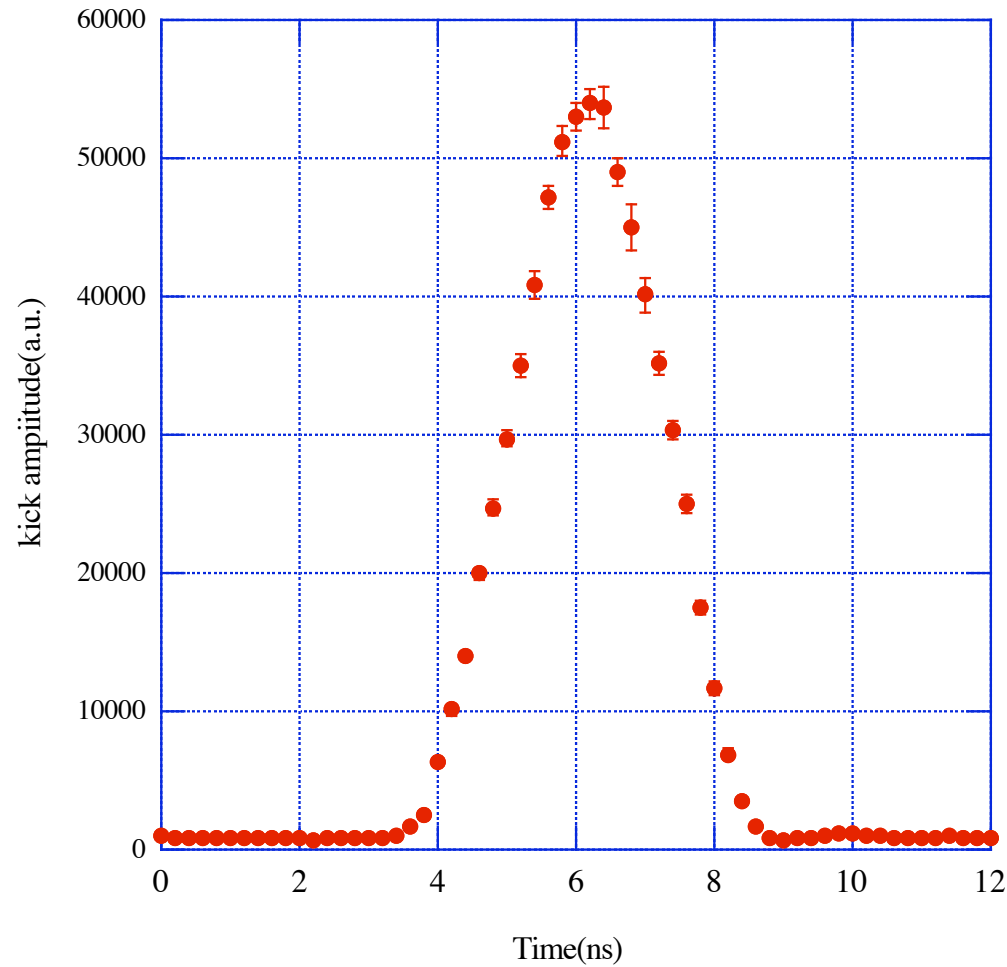
The beam kick is observed by a turn-by-turn BPM as the amplitude of the oscillation of the betatron frequency component.

The kick effect is measured by scanning the pulse timing.



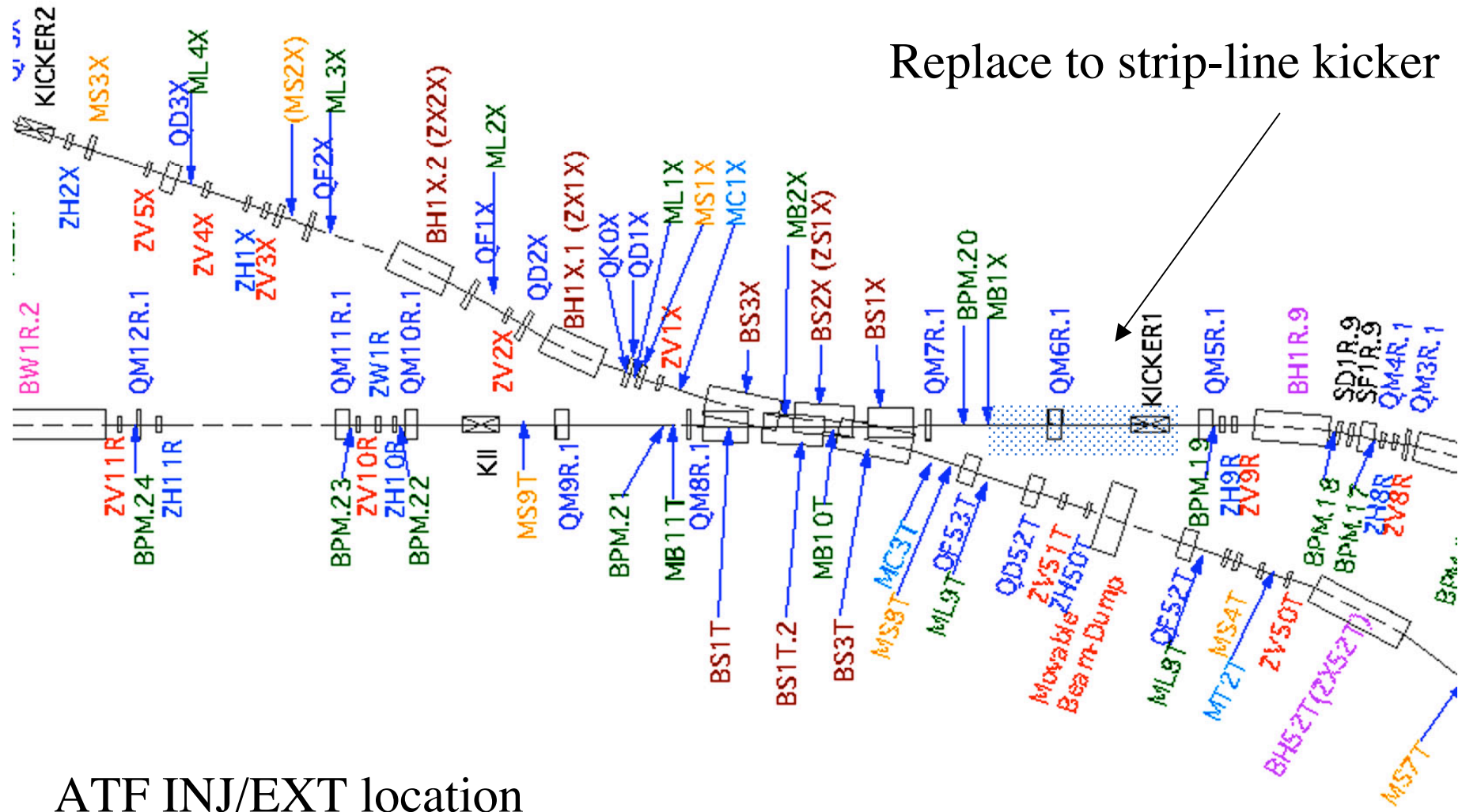
Measurement result of FPG5-3000M

Beam kick profile(30cm strip-line, 5kV FID pulser)

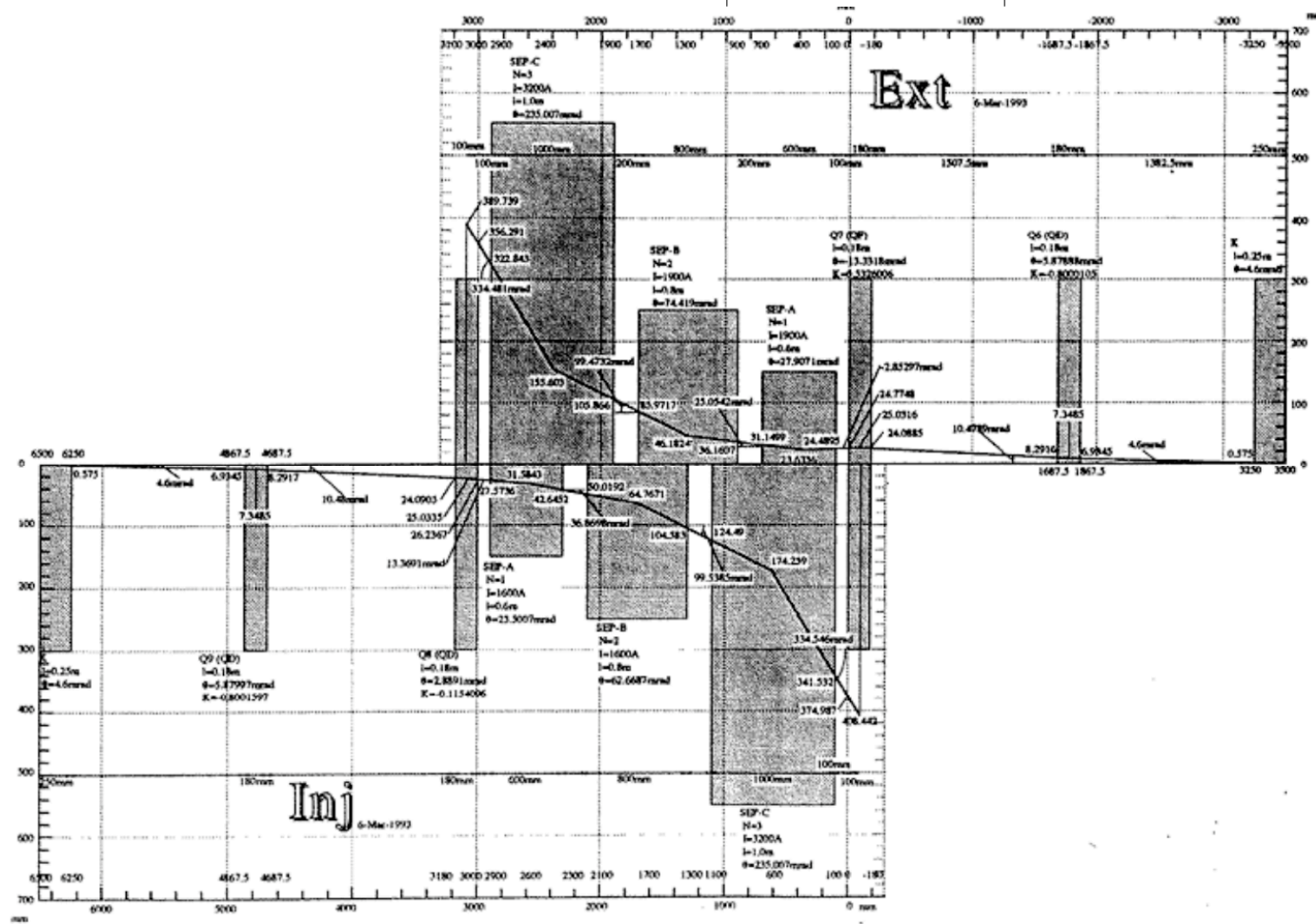
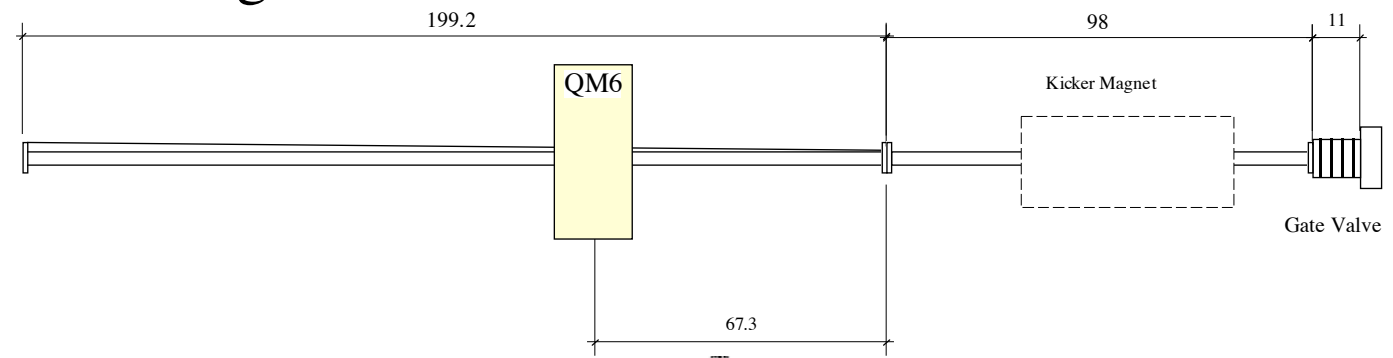


Rise time~3.2ns
Kick angle ~85 μ rad
(calc. 94.7 μ rad)

Beam Extraction Test



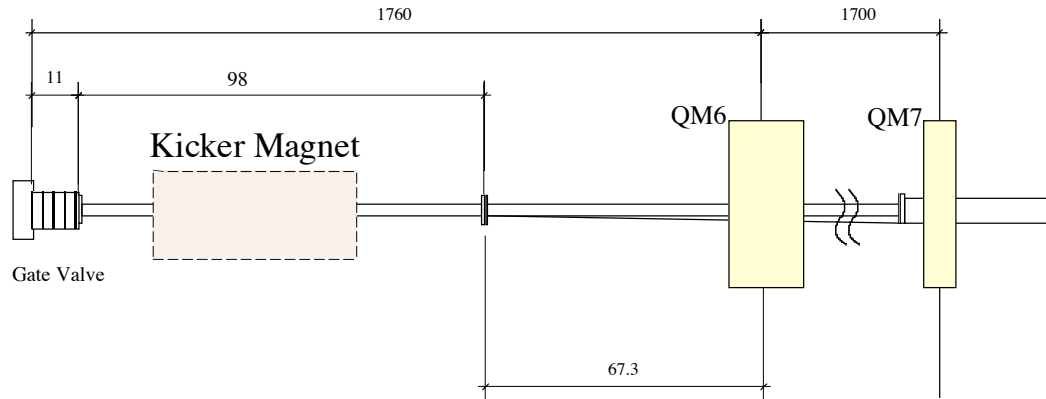
Present layout and design orbit



Orbit Design

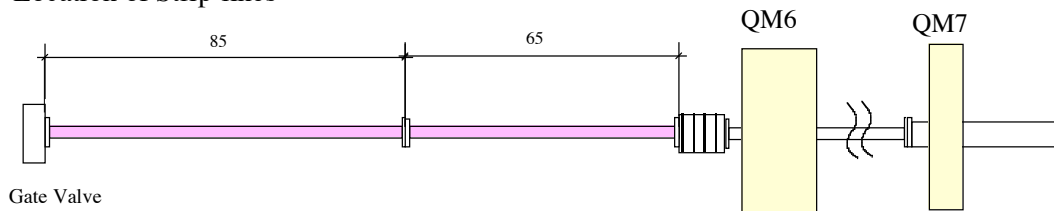
20051017 T.Naito

Present layout

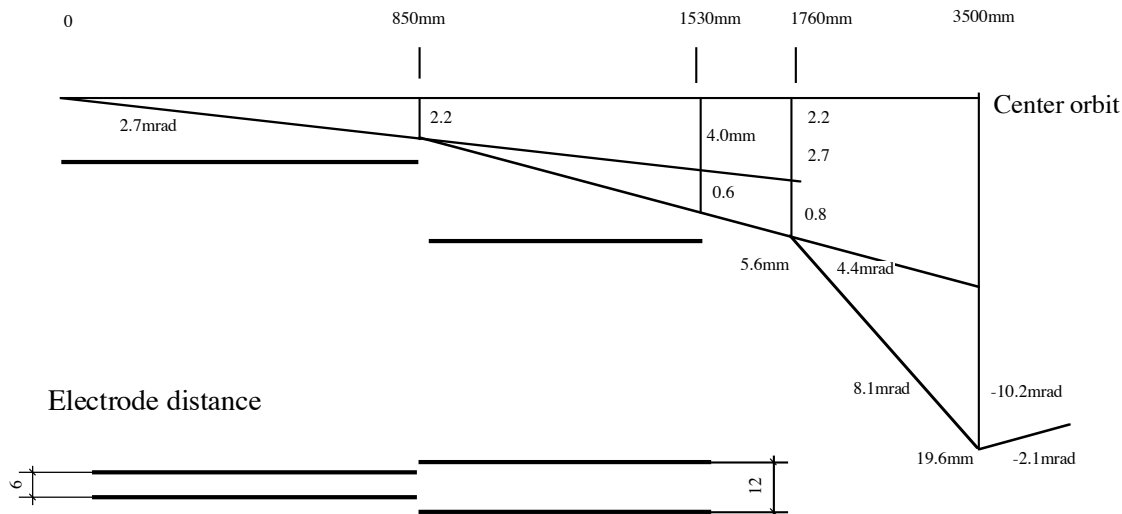


Single pulser +
1mx2 strip-lines

Location of Strip-lines

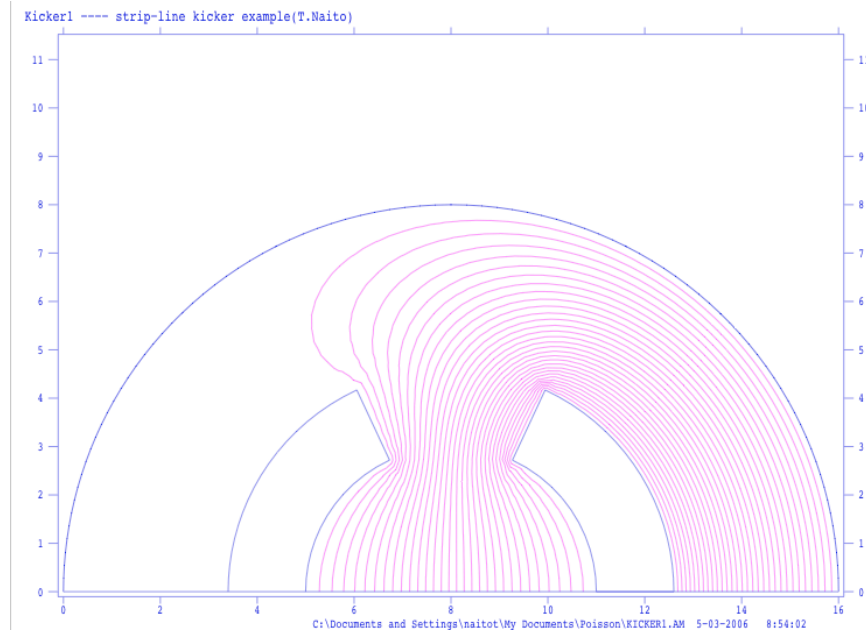


Design Orbit

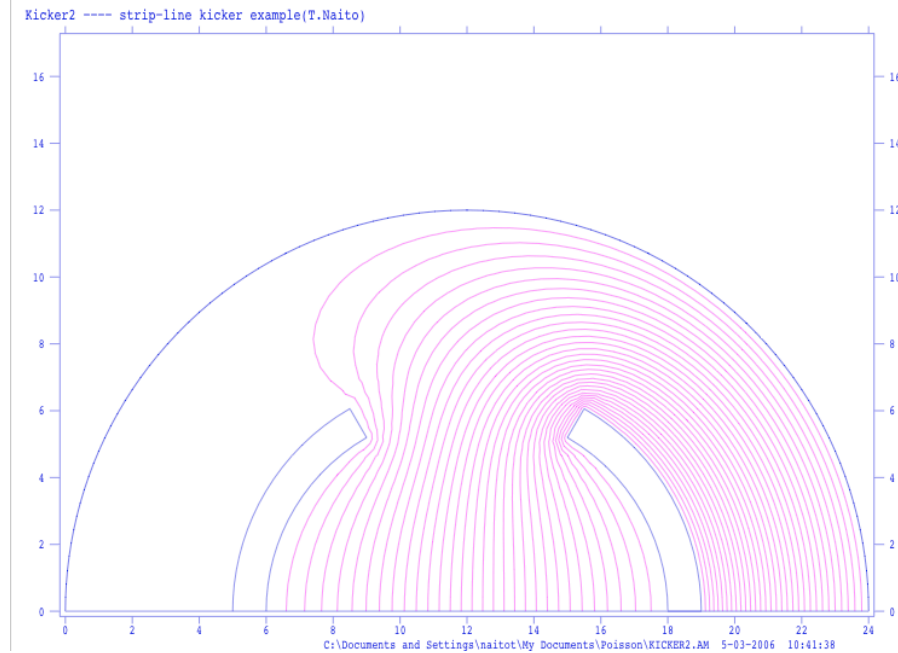


Strip-line Electrode design

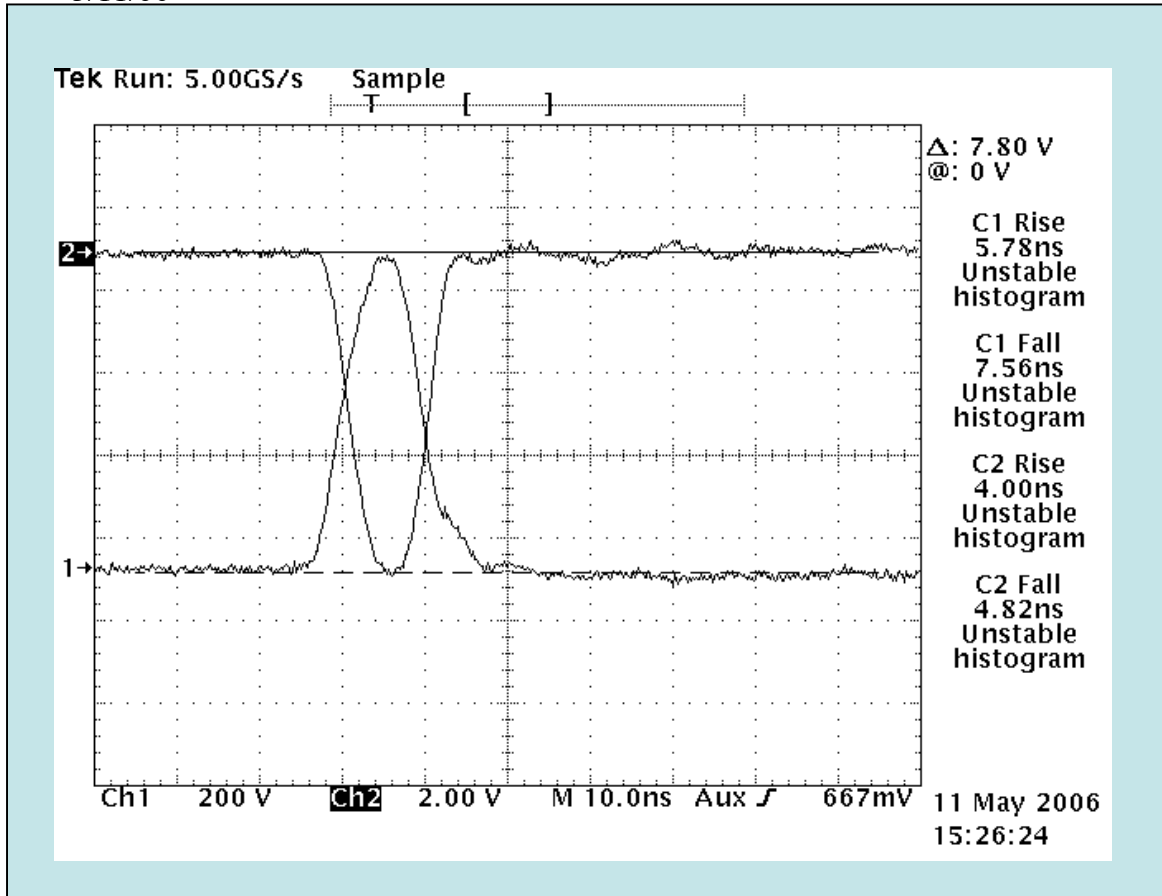
6mm ϕ



12mm ϕ



Data Taken By: LLNL pulser
Ed Cook
5/11/06



Inductive Adder pulser

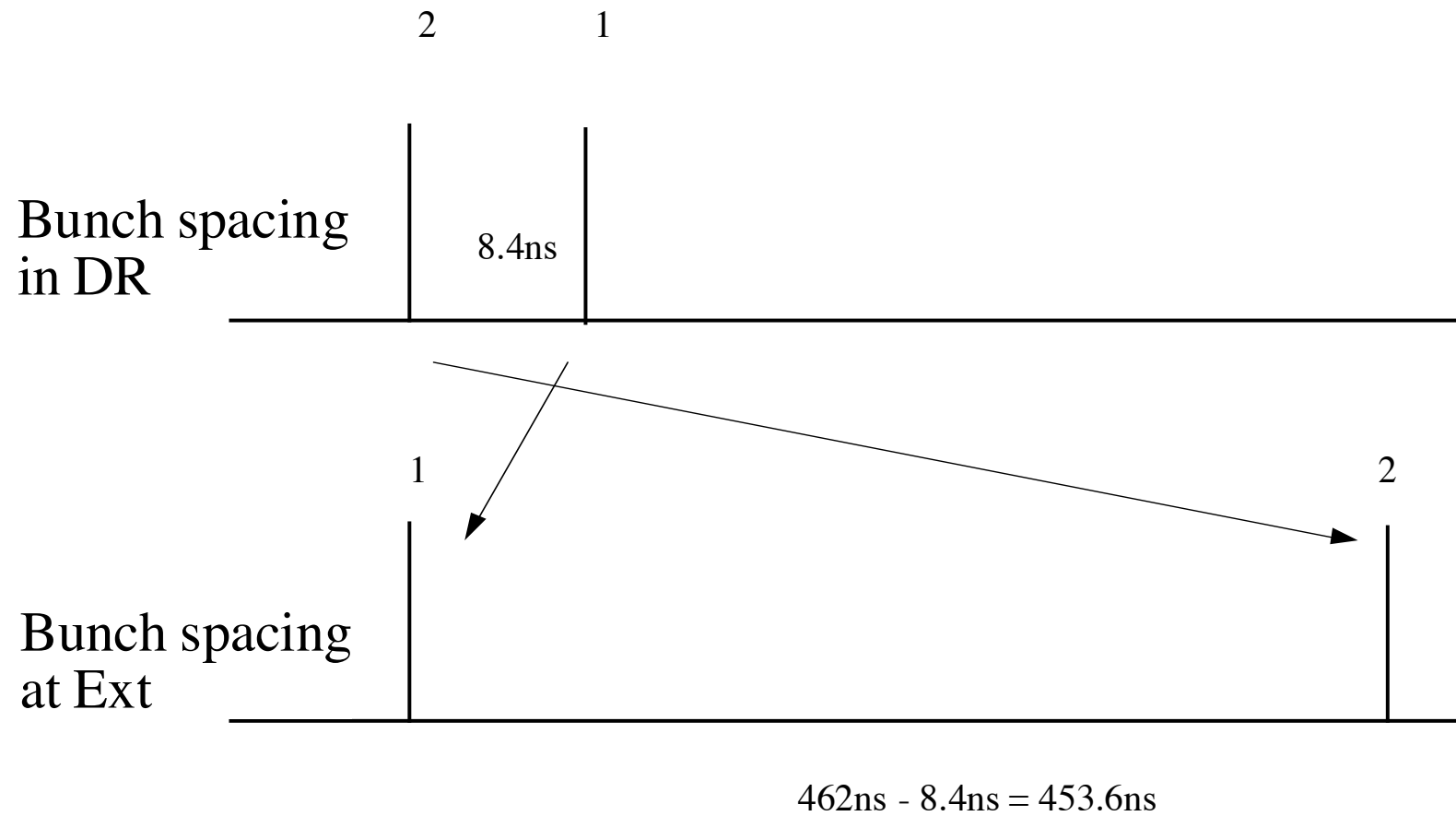
The stacked FET boards makes the high speed and high voltage pulse.

Rise time ~ 5.7 ns

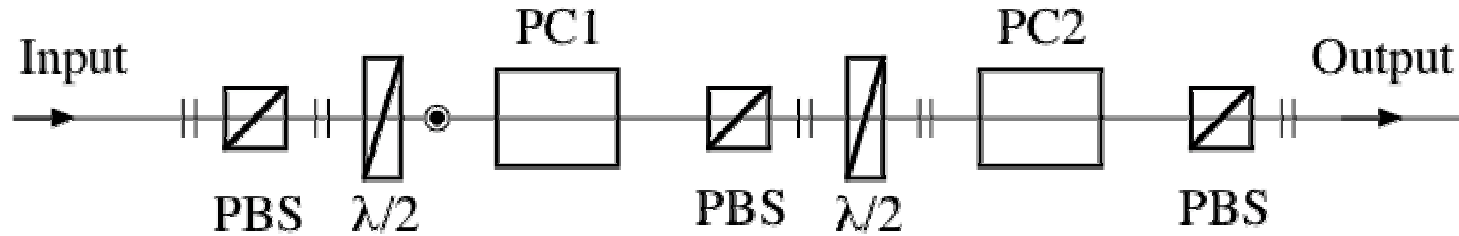
$V_p \sim \pm 8$ kV



Inj/Ext Beam

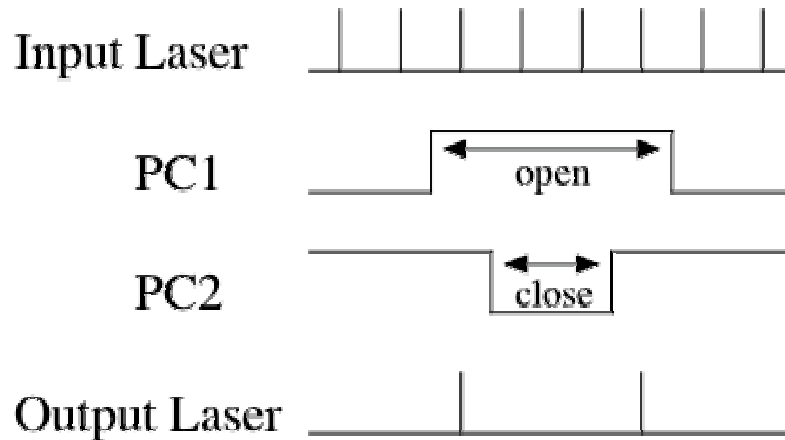


Laser modification for Two bunches beam generation with arbitrary bunch spacing



PC: Pockels Cell

PBS: Polarizing beam splitter



Future Plan

